

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Takefumi NISHIMUTA, Hiroshi MIYAGI, Tadahiro OHMI, Shigetoshi SUGAWA, and Akinobu TERAMOTO

Serial No.: TBA Group Art Unit: TBA

Filed: Herewith Examiner: TBA

For: DC AMPLIFIER AND SEMICONDUCTOR INTEGRATED CIRCUIT THEREFOR

Customer No.: 27123

INFORMATION DISCLOSURE STATEMENT

Mail Stop PCT
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Alexandria, VA 22313-1450

Sir:

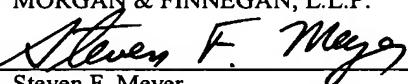
Pursuant to Rule 56, applicant hereby calls the attention of the Patent Office to the references listed on the attached Form PTO 1449. Copy(ies) of these references are attached were filed in related application U.S. Serial No(s) _____, filed _____, respectively.

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Respectfully submitted,
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Dated: December 13, 2005

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10/560704
IAP9 Rec'd PCT/PTO 13 DEC 2005

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FORM PTO-1449 INFORMATION DISCLOSURE CITATION			Attorney Docket No. 5000-5296	Serial No.: To Be Assigned		
			Applicants: Takefumi NISHIMUTA, Hiroshi MIYAGI, Tadahiro OHMI, Shigetoshi SUGAWA, and Akinobu TERAMOTO			
			Filing Date: Herewith	Group Art Unit: To Be Assigned		
			U.S. PATENT DOCUMENTS			
Examiner Initial	Patent Number	Publication Date	Name	Class	Sub-Class	Filing Date
	US 2002/0011612 A1	January 31, 2002	Hieda	257	262	July 30, 2001
FOREIGN PATENT DOCUMENTS						
Examiner Initial	Patent Number	Publication Date	Country	Class	Sub-Class	Translation
	JP-A-2002-261091	September 13, 2002	Japan	H 01 L	21 / 316	Accompanied by English abstract
	JP-A-2002-217769	August 2, 2002	Japan	H 04 B	1 / 30	Accompanied by English abstract
	JP-A-2002-118255	April 19, 2002	Japan	H 01 L	29 / 78	Accompanied by corresponding US Patent publication 2002/0011612 A1
	JP-A-2002-261097	September 13, 2002	Japan	H 01 L	21 / 318	Accompanied by corresponding EP Patent publication EP 1 347 506 A1
	EP 1 347 506 A1	September 24, 2003	EPO	H 01 L	21 / 316	English language version (Corresponding to JP-A-2002-261097)
	JP-A-H04-154312	May 27, 1992	Japan	H 03 F	3 / 45	Accompanied by English abstract
OTHER DOCUMENTS (Including Author, Title, Date, etc.)						
Examiner			Date Considered			
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